PRODUCT CHANGE NOTIFICATION (PCN)

(According to JEDEC standard J-STD-046)

PCN number: GC244583 Date: 11 December 2024

Title: Replacement of 4GB Rev A DDR4 with 4GB Rev B DDR4

Product identification: All DDR4 revision A references

Product Name	Radiation Performance	DDR Size	Bus Width	Temperature Range	Package Type	Speed (MT/s)	Revision	Grade
DDR4	T:	04G: 4 GByte	72: - 72 bits	M: -55/125C	ZR: PBGA Stacked Wire Bond (Leaded SnPb)	2133 and 2400	A	EM: Engineering Models EQM: Engineering Qualification Models -N1: Nasa Level 1 -N2: Nasa Level 2 -N3: Nasa Level 3
	Rad Tol	04G: 4 GByte 08G: 8 GByte		A: -40/105C	ZS: PBGA Stacked Wire Bond (Leadfree RoHS)	PBGA tacked re Bond eadfree (a) 2400 B -E1: ECSS C -E2: ECSS C -E3: ECSS C -X1: Specific	-E1: ECSS Class 1 (b) -E2: ECSS Class 2 (b) -E3: ECSS Class 3 -X1: Specific screening	

(a) Only 2400 MT/s will be available in revB and is backward compatible with 2133MT/s

(b) E1 and E2 no more offered in Rev. B

For example, DDR4T04G72AZR2AEM is redirected to DDR4T04G72AZR2BEM

See also our dedicated web page with link to product datasheet tps://semiconductors.teledyne-e2v.com/en/products/memory/space-radiation-tolerant-4gb-8gb-ddr4/

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Reason for change: Manufacturing flow and materials evolutions						
□Design	□Material	□Processing	⊠Other (wafer die revision)			
□Logistics	☐ Manufacturing Location	Manufacturing Location Quality/Reliability				
Change description: Due to Rev A inventory depletion, Teledyne e2v announces the replacement of DDR4 revision A by DDR4 Revision B, that has been successfully qualified.						
Identification method to distinguish change: The product change will be identified by the product part number reference and marking.						



Qualification	on data:	☑ Already available	□Will be available	☐ Not applicable			
Qualification samples:		☐ Already available	⋈ Not applicable				
Quantifiable impact & reliability: Revision A and Rev. B are footprint, form & function compatible.							
Please refer to the Datasheet for pin out specific assignment. Please refer to Application Note "AN 60S 225419 DDR4 - Rev A to B Migration Recommendations"							
Implementation date: Immediate							
The Estimated Implementation Date is the forecasted date that a customer may expect to receive changed product. This is determined by the estimated date of inventory depletion on the PCN issue date. This may be affected by fluctuations in supply and demand. Consequently, although customers should be prepared to receive changed product on this date, Teledyne will continue to ship pre-changed product until a time in which inventory has been depleted. This may result in pre-changed product being shipped to customers after this forecasted date.							
Approved	Quality		Marketing	Marketing			
by:	Name: M.BAL Mail: hotline-	L std@teledyne.com	Name: L.GIRY Mail: semiconductors.mkt@teledyne.com				

Teledyne will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice. All correspondence must be sent to the contact e-mail addresses indicated above. Teledyne assumes no responsibility for any errors that may appear in this document.

The supply of products will be subject to Teledyne general terms and conditions of sale or any specific contractual terms agreed between the parties.